

EV077332066

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

7/2/02
#9/105

Application Serial No. 09/885,393
 Filing Date June 19, 2001
 Inventor Richard Holscher et al.
 Assignee Micron Technology, Inc.
 Group Art Unit 1756
 Examiner Unknown
 Attorney's Docket No. MI22-1694
 Title: Semiconductor Processing Methods

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**


References -- See Attached Form PTO-1449

The attached Form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether the submitted references are prior art.

This Supplemental Information Disclosure Statement is being filed within three months of the filing of the application or before the mailing of a first Office Action on the merits, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. Section 1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Respectfully submitted,

Dated: 6-24-02

 Attorney: 
 D. Brent Kenady
 Reg. No. 40,045

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1449

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Respectfully submitted,

Dated: 4-23-02

Attorney: 
D. Brent Kenady
Reg. No. 40,045

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Respectfully submitted,

Dated: 1-28-02

Attorney: 
D. Brent Kenady
Reg. No. 40,045

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/885,393
Filing Date June 19, 2001
Inventor Richard Holscher et al.
Assignee Micron Technology, Inc.
Group Art Unit 1756
Examiner Unknown
Attorney's Docket No. MI22-1694
Title: Semiconductor Processing Methods



SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

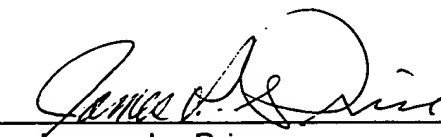
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Respectfully submitted,

Dated: 12/3/01

Attorney: 
James L. Price
Reg. No. 27,376

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/885,393
Filing Date June 19, 2001
Inventor Richard Holscher et al.
Assignee Micron Technology, Inc.
Group Art Unit 1756
Examiner Unknown
Attorney's Docket No. MI22-1694
Title: Semiconductor Processing Methods



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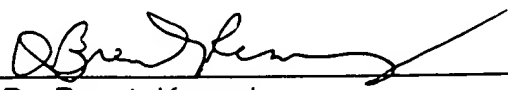
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Respectfully submitted,

Dated: 8-21-01

Attorney: 
D. Brent Kenady
Reg. No. 40,045

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/885,393
Filing Date June 19, 2001
Inventor Richard Holscher et al.
Assignee Micron Technology, Inc.
Group Art Unit 1756
Examiner Unknown
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Title: Semiconductor Processing Methods



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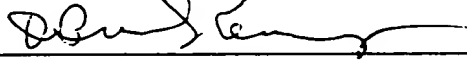
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Respectfully submitted,

Dated: 7-30-01

Attorney: 
D. Brent Kenady
Reg. No. 40,045

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 09/030,618
Priority Filing Date February 25, 1998
Inventor Richard Holscher et al.
Assignee Micron Technology, Inc.
Priority Group Art Unit 1756
Priority Examiner K. Duda
Attorney's Docket No. MI22-1694
Title: Semiconductor Processing Methods

**INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449


The attached form PTO-1449 is submitted in compliance with 37 CFR §1.56. No admission is made regarding whether the submitted references are prior art.

The listed references were cited by, or submitted to, the Office in the parent, co-pending application of the above-identified application. The above-identified application is a continuation of co-pending Application Serial No. 09/030,618, filed on February 25, 1998. Such prior disclosure is sufficient for the above-identified application as far as copies of the references are concerned.

Citation of these references is respectfully requested.

Respectfully submitted,

Dated: 6-19-01

Attorney: 
D. Brent Kenady
Reg. No. 40,045